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TC 2800 MAIL ROOM
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Akihiro Nitayama)
SERIAL No. 09/660,390)
FILED: September 12, 2000)
ATTN: APPLICATION DIVISION
Group Art Unit: 2814
Attorney Docket No. 00629.00002

For: SEMICONDUCTOR MEMORY DEVICE AND METHOD OF MANUFACTURING THE SAME

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

It is requested that a corrected filing receipt be issued to correct errors found in the official filing receipt. Specifically, the second Applicant's last name is misspelled. Also the residence of the first and third Applicants are incorrect. In the Applicants' data, please delete the second Applicants last name "Katsukiko" and insert --Katsuhiko--. Also delete "Yokohama-shi" as the residence of the first Applicant and insert --Kawasaki-shi--. Also for the residence of the third Applicant please delete "Yokosuka-shi" and insert --Yokohama-shi--.

A copy of the official filing receipt with corrections marked in red is attached. Also enclosed is a copy of the filed and executed declaration indicating the spelling of the Applicants names and correct residences. No fee is believed to be associated with this paper since this error occurred through no fault of the Applicants. Nonetheless, should the U.S. PTO determine that a fee is required, please charge such fee to our Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

By: Steve D # 42,402
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Dated: April 12, 2001

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/660,390	09/12/2000	2814	1240	00629.00002	54	17	8

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BANNER & WITCOFF LTD.

FILING RECEIPT



OC00000005875445

Date Mailed: 03/19/2001

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Applicant(s)

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Shigeru Ishibashi, Yokosuka-shi, JAPAN; Yokohama-shi
Yusuke Kohyama, Yokosuka-shi, JAPAN;

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Continuing Data as Claimed by Applicant

THIS APPLICATION IS A CIP OF 08/982,478 12/02/1997

Foreign Applications

If Required, Foreign Filing License Granted 11/29/2000

Title

Semiconductor memory device and method of manufacturing the same

Preliminary Class

257

Data entry by : MASON, JAMES

Team : OIPE

Date: 03/19/2001



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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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